Docket No. 5000-5296

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	: Takefumi NISHIMUTA, et al.	Confirmation No.:	9335
Serial No.:	10/560,704	Group Art Unit:	2826
Filed:	December 13, 2005	Examiner:	QUACH, Tuan N.
For:	DC AMPLIFIER AND SEMICONDUCTOR INTEGRATED CIRCUIT THEREFOR		
PETITION AND FEE FOR EXTENSION OF TIME (37 C.F.R. § 1.136(a))			
Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450			
Sir:			
 This is a petition for an extension of time for filing a response to the Office Action dated March 29, 2007. 			
 The communication in connection with the matter for which this extension is requested is filed herewith. is being filed concurrently herewith. Applicant(s) is/are entitled to Small Entity Status. Is Statement has already been filed 			
a.	Total Months Requested one month two months three months four months five months An extension for months identified communication and th from the total fee due for the tots fee for this extension (\$ \$), equals \$ (total fee due.)	e fee paid therefor of \$	is deducted now requested. The
5. 🔲 A	A check in the amount of \$ to cover the extension fee is attached.		
6. 🖂 Ch	Charge fee to Deposit Account No. 13-4500, Order No. 5000-5296.		

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 In Commissioner is hereby authorized to charge any additional fees which may be required by this paper, or credit any overpayment to Deposit Account No. 13-4500. Order No. 5000-5296.

> Respectfully submitted, MORGAN & FINNEGAN, L.L.P.

Dated: July 26, 2006

Steven F. Meyer

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